FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

ATTY DOCKET NO.

30585/6

APPLICANT

David L. Reese, et al.

FILING DATE

GROUP ART UNIT

2751

INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)

(0)		U.S. P	ATENT DOCUME	NTS		
1999EX MINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
ARY CE CO	3,544,969	12/01/70	Rakoczi	340	172.5	Nov. 27, 1967
C	3,781,823	12/25/73	Senese	340	172.5	July 28, 1972
d	4,084,235	04/11/78	Hirtle	364	200	Apr. 14, 1975
cd	4,455,602	06/19/84	Baxter	364	200	May 22, 981
c d	4,575,797	03/11/86	Gruner	364	200	May 22, 1981
ره	5,638,525	06/10/97	Hammond	395	385	Feb. 10, 1995
cd	5,822,578	10/13/98	Frank	395	591	June 5, 1995
d	5,832,205	11/03/98	Kelly	395	185.06	Aug. 20, 1996
ed	5,930,509	07/27/99	Yates	395	707	Jan. 29, 1996
$\mathcal{C}_{\mathcal{A}}$	5,958,047	09/28/99	Panwar	712	237	Jun. 25, 1997

May 28, 1999

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION ED
	WO 96/24895	08/15/96	PCT/US	G06F	9/22	0 1000
						NOV 3 0 1999
						Group 2700

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)

Cd	Burroughs, Preliminary Edition, B 1700 Systems Reference Manual, Burroughs Corporation (1972)
cd	Burroughs, B 1700 Systems Micro Implementation Language (MIL) Reference Manual, Burroughs Corporation (1973)
Cel	Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)
U	T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29 th Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)
ad	T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)
Cd	T.M. Conte, et al., "Using branch handling hardware to support profile-driven optimization," Proceedings of the 27 th Annual International Symposium on Microarchitecture San Jose, CA (Dec. 1994)
CV	J.S. Cox et al., "Commercializing profile-driven optimization." Proceedings of the 28 th Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)

EXAMINER

chil. c.s

DATE CONSIDERED

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

	•				
•	FORM PTO-	1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		
	INFORMATION DISCLOSURE CITATION				
OIPE	v:	(Us	Use several sheets if necessary)		
NOV 2 6 1999	8	ОТНІ	ER DOCUMENTS (Including A		
PADEMARY	901		Digital Equipment Corp., White http://www.digital.com/semico		
MADEMI	(d		Ebcioglu and Altman, IBM Reservante Architectural Compatibility, IBM		
	Cd		Ronald M. Guffin, Microdiagnos IEEE Transactions on Computers		
	cd		Linley Gwennap, MDR Technica Technology and Market Forecas Microprocessor Report, MicroDe		
	0		Linley Gwennap, MDR Technica		

ATTY. DOCKET NO. 30585/6

SERIAL NO. 09/322,443

APPLICANT

David L. Reese, et al.

FILING DATE

GROUP ART UNIT

	Ose several sneets if necessary)	May 28, 1999	2751					
g & OTI	HER DOCUMENTS (Including	g Author, Title, Date, Pei	rtinent Papers, Etc.)					
	Digital Equipment Corp., Wh http://www.digital.com/ser		FX!32 Works (September 1997) -white.htm					
100	Ebcioglu and Altman, IBM Research Report, DAISY: Dynamic Compilation for 100% Architectural Compatibility, IBM Research Division (1996)							
d	Ronald M. Guffin, Microdiagnostics for the Standard Computer MLP-900 Processor, IEEE Transactions on Computers, Vol. C-20 No. 7, pp. 803-808 (July 1971)							
cd	Technology and Market Fore	Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64 Technology and Market Forecast, IA-64 Software Model - Chapter 3 excerpt Microprocessor Report, MicroDesign Resources (July 20, 1998)						
d	Linley Gwennap, MDR Tech Technology and Market Fore MicroDesign Resources (July	cast, Executive Summary,	•					
d			ocument Reveals Processors With sign Resources (July 20, 1998)					
cd		R.E. Hank, et al., Proceeding of the 28 th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)						
ECENED		Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM						
NOV 3 0 1999		Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)						
Group 2700		K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University,						
cd	Nanodata Corporation, QM-1	Hardware level User's Ma	nual (1975)					
. cd	Nanodata Corporation, QM-2 Processor (1976)							
cd	Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)							
d	Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)							
W	Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated – possibly 1969)							
cd	W.T. Wilner, Design of the E 497 (1972)	Burroughs B1700, Fall Join	t Computer Conference, pp. 489-					
J	W.T. Wilner, Burroughs B17 pp. 579-586 (1972)	00 Memory Utilization, Fa	ll Joint Computer Conference,					
EXAMINER	0 1 - n	DATE CONSIDER	ED Af					

lul. c.b

Melos

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

FORM PTO-1449

U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

SERIAL NO. ATTY. DOCKET NO 09/322,443 30585/6 APPLICANT

FORMATION DISCLOSURE CITATION

David L. Reese, et al. GROUP ART UNIT FILING DATE 2751

	(Use several sheets if nece	essary)	FILING DATE May 28, 1999)	2751	
		U.S. PATE	ENT DOCUMENT	TS.		FILING DATE
			NAME	CLASS	SUBCLASS	IF APPROPRIATE
(AMINER INITIAL	DOCUMENT NUMBER	8/18/98	Berc, et al.	395	184.01	Mar. 10, 1997
R	5,796,939	9/1/98	Sites, et al.	395	183.21	Dec. 19, 1994
The state of the s	5,802,272	9/1/96			RECE	IN/ED
06 100						
1999 (_		DEC 0	7 1999
PARK (FERICE					Group	2700
						P. W. Commission of the Commis
		FODEICN	PATENT DOCUM	MENTS		700
		1900 - 1900 T. J. 1800 A. C 25	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	DOCUMENT NUMBER	DATE				
	1	1				\
				\		
		TENTS (Inch	iding Author, Title,	Date, Perti	nent Paper.	s, Etc.)
	OTHER DOCUM	MENTS (Inclu	iding Author, Title,	Date, Perti	nent Paper.	s, Etc.)

						Sheet 1 of 2	
FORM PTO-14		MENT OF COMMERCE TRADEMARK OFFICE	ATTY. DOCKET NO. 30585/6		SERIAL NO. 09/322,4		
INFORMATION DISCLOSURE CITATION			1	APPLICANT David I. Poons et al.			
(Use several sheets if necessary)			David L. Reese, et al. FILING DATE GROUP ART UNIT				
ner 2	2 3 2000 14			May 28, 1999 2751			
B	3,544,969	U.S. PATI	ENT DOCUMENT	rs .			
EXAMINATION INITIAL	DOCEMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
Cd	3,544,969	12/01/1970	Rakoczi	340	172.5	Nov. 27, 1967	
d	3,781,823	12/25/1973	Senese	340	172.5	July 28, 1972	
(8)	4,084,235	04/11/1978	Hirtle	364	200	Apr. 14, 1975	
CA	4,455,602	06/19/1984	Baxter	364	200	May 22, 981	
d	4,575,797	03/11/1986	Gruner	364	200	May 22, 1981	
الحي	5,542,109	07/30/1996	Blomgren	395	800	Aug. 31, 1994	
cd	5,638,525	06/10/1997	Hammond	395	385	Feb. 10, 1995	
	5,796,939	08/18/1998	Berc, et al.	395	184.01	Mar. 10, 1997	
e d	5,802,272	09/01/1998	Sites, et al.	395	183.21	Dec. 19, 1994	
c d	5,802,373	09/01/1998	Yates	395	705	Jan. 29, 1996	
id	5,822,578	10/13/1998	Frank	395	591	June 5, 1995	
d	5,832,205	11/03/1998	Kelly	395	185.06	Aug. 20, 1996	
i.d	5,842,017	11/24/1998	Hookway	395	707	Jan. 29, 1996	
d	5,930,509	07/27/1999	Yates	395	707	Jan. 29, 1996	
	5,949,415	09/07/1999	Lin	345	335	Jun. 16, 1997	
	5,958,047	09/28/1999	Panwar	712	237	Jun. 25, 1997	
3	6,000,028	12/07/1999	Chernoff	712	226	Jan. 29, 1996	
C)	6,091,897	07/18/2000	Yates	395	708	Jan. 29, 1996	
	OTHER DO	OCUMENTS (Includir	ng Author, Title, Do	ite, Pertine	ent Papers,	Etc.)	
d						ghs Corporation (1972)	
d	Burroughs, B Corporation (mplementation Lan	iguage (M	L) Referei	nce Manual, Burroughs	
cd		Anton Chernoff, et al., FX!32, A Profile-Directed Binary Translator, IEEE Micro, Vol. 18 No. 2 pp. 56-64 (March/April, 1998)					
ce	Compaq Com	Compaq Computer Corp., Compiler Writer's Guide for the Alpha 21264, 1999					
<u>d</u> .	Proceedings of	T.M. Conte, et al., "Accurate and practical profile-driven compilation using the profile buffer," Proceedings of the 29 th Annual International Symposium on Microarchitecture, Paris, France, pp. 36-45 (Dec. 1996)					
d	1	T.M. Conte, et al., "Hardware based profiling: An effective technique for profile-driven optimization," International Journal of Parallel Programming, vol. 24, no. 2 (Feb. 1996)					
d		et al., "Using branch ha of the 27 th Annual Inter				en optimization," are San Jose, CA (Dec.	
EXAMINER	chil	c. Dr	DATE CONSIDER	RED U	93		

FORM PTO-1	449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY, DOCKET NO. 30585/6	SERIAL NO. 09/322,443		
ASFORM	1A P	ON DICCI OCUDE CITATION	APPLICANT David L. Reese, et al.			
) oom	Several sheets if necessary)	FILING DATE	GROUP ART UNIT		
T WILL 5 ,			May 28, 1999	2751		
E.	OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Papers, Etc.)					
THA CONTRACTOR	DEME	J.S. Cox et al., "Commercializing profile-driven optimization." Proceedings of the 28 th Hawaii International Conference on System Sciences, vol. 1 Maui, HI, pp. 221-228 (Jan. 1995)				
d		Digital Equipment Corp., White Paper: http://www.digital.com/semiconduc	:: How DIGITAL FX!32 Works (September 1997) ctor/amt/fx32/fx-white.htm			
الم		Ebcioglu and Altman, IBM Research R Compatibility, IBM Research Division		mpilation for 100% Architectural		
<u></u>		Linda Geppert et al., Transmeta's Mag	ic Show, IEEE Spectrum, vo	l. 37 no. 5, pp. 26-33 (May 2000)		
d		Ronald M. Guffin, Microdiagnostics fo Transactions on Computers, Vol. C-20	•	*		
d		Linley Gwennap, MDR Technical Libr Market Forecast, IA-64 Software Mode Resources (July 20, 1998)				
٨		Linley Gwennap, MDR Technical Library Special Report, Intel's Merced and IA-64: Technology and Market Forecast, Executive Summary, Microprocessor Report, MicroDesign Resources (July 20, 1998)				
d		Linley Gwennap, First Merced Patent Surfaces, Intel Document Reveals Processors With Dual Instruction Sets, Microprocessor Report, MicroDesign Resources (July 20, 1998)				
d		R.E. Hank, et al., Proceeding of the 28 th Annual International Symposium on Microarchitecture, pp. 158-168 (Dec. 1995)				
d		Raymond J. Hookway, et al., Combining Emulation and Binary Translation (August 28, 1997) http://www.digital.com/info/DTJP01HM.HTM				
d.		Harold W. Lawson, Jr., et al., Functional Characteristics of a Multilingual Processor, IEEE Transactions on Computers, Vol. C-20, No. 7, pp. 732-742 (July, 1971)				
d		K.N.P. Menezes, "Hardware-based profiling for program optimization," Ph.D. thesis, Department of Electrical and Computer Engineering, North Carolina State University, Raleigh, North Carolina (1997)				
()		Nanodata Corporation, QM-1 Hardwar	e level User's Manual (1975))		
		Nanodata Corporation, QM-2 Processo	r (1976)			
Ca		Robert F. Rosin, Contemporary Concepts of Microprogramming and Emulation, Computing Surveys, Vol. 1 No. 4, pp. 197-212 (December 1969)				
<u>.</u>		Robert F. Rosin, An Environment for Research in Microprogramming and Emulation, Communications of the ACM, Vol. 15 No. 8, pp. 748-760 (August 1972)				
d		Standard Computer Corp., IC-9000 Central Processing Unit - Principles of Operation (undated – possibly 1969)				
(A)		W.T. Wilner, Design of the Burroughs	B1700, Fall Joint Computer	Conference, pp. 489-497 (1972)		
d do		W.T. Wilner, Burroughs B1700 Memory Utilization, Fall Joint Computer Conference, pp. 579-586 (1972)				
<i>J</i>						
EXAMINE	R	chil. C M DA	ATE CONSIDERED WE	63		